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#6   (substrate<sentence>coupling<sentence>noise)<paragraph>model* and
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#8   (substrate<sentence>coupling<sentence>noise)<paragraph>model* and
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☐   1. **An efficient modeling approach for substrate noise coupling analysis**
Ozis, D.; Mayaram, K.; Fiez, T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 5, 26-29 May 2002 Page(s):V-237 - V-240 vol.5
Digital Object Identifier 10.1109/ISCAS.2002.1010684

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☐   2. **A comprehensive geometry-dependent macromodel for substrate noise coupling in heavily ·
processes**
Ozis, D.; Fiez, T.; Mayaram, K.;
Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002
12-15 May 2002 Page(s):497 - 500
Digital Object Identifier 10.1109/CICC.2002.1012887

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☐   **1. Wavelets modeling of inductance of interconnect in digital circuits of mixed-signal ICs**
Wang Jun;
Microwave Conference Proceedings, 2005. APMC 2005. Asia-Pacific Conference Proceedings
Volume 2, 4-7 Dec. 2005 Page(s):5 pp.
Digital Object Identifier 10.1109/APMC.2005.1606515

AbstractPlus | Full Text: PDF(1680 KB) IEEE CNF
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☐   **2. A sparsified vector potential equivalent circuit model for massively coupled interconnects**
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23-26 May 2005 Page(s):105 - 108 Vol. 1
Digital Object Identifier 10.1109/ISCAS.2005.1464535

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☐   **3. Multilevel integral equation methods for the extraction of substrate coupling parameters in**
Chou, M.; White, J.;
Design Automation Conference, 1998. Proceedings
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☐   **4. Methodology for expedient computation of semiconductor substrate noise coupling**
Manetas, G.; Cangellaris, A.C.;
Electrical Performance of Electronic Packaging, 2004. IEEE 13th Topical Meeting on
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☐   **5. A simple transmission line de-embedding method for accurate RF CMOS noise modeling**
Guo, J.C.; Huang, C.H.; Lien, W.Y.; Wu, C.M.;
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7. Stable and efficient reduction of large, multiport RC networks by pole analysis via congruen
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9. Efficient techniques for accurate modeling and simulation of substrate coupling in mixed-si:
Costa, J.P.; Mike Chou; Silveira, L.M.;
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Volume 18, Issue 5, May 1999 Page(s):597 - 607
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10. An efficient formulation for substrate parasitic extraction accounting for nonuniform curren:
Cheng-gang Xu; Fiez, T.; Mayaram, K.;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuits and Systems I: F(
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Koukab, A.; Banerjee, K.; Declercq, M.;
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13. Using MMAP for mixed signal empirical substrate design optimization
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Lan, H.; Dutton, R.;
Design, Automation and Test in Europe Conference and Exhibition, 2004. Proceedings
Volume 2, 16-20 Feb. 2004 Page(s):836 - 841 Vol.2
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19. SuPREME: substrate and power-delivery reluctance-enhanced macromodel evaluation
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21. **Efficient techniques for accurate modeling and simulation of substrate coupling in mixed-sig**
Costa, J.P.; Chou, M.; Silveira, L.M.;
Design, Automation and Test in Europe, 1998., Proceedings
23-26 Feb. 1998 Page(s):892 - 898
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22. **Cell-level placement for improving substrate thermal distribution**
Ching-Han Tsai; Sung-Mo Kang;
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Weisshaar, A.; Hai Lan; Luoh, A.;
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☐   1. **MOS transistor modeling for RF IC design**
        Enz, C.; Cheng, Y.;
        Solid-State Circuits, IEEE Journal of
        Volume 35, Issue 2, Feb. 2000 Page(s):186 - 201
        Digital Object Identifier 10.1109/4.823444

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        Samavedam, A.; Sadate, A.; Mayaram, K.; Fiez, T.S.;
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☐   3. **A design system for RFIC: challenges and solutions**
        Miliozzi, P.; Kundert, K.; Lampaert, K.; Good, P.; Chian, M.;
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        Enz, C.;
        Microwave Theory and Techniques, IEEE Transactions on
        Volume 50, Issue 1, Part 2, Jan. 2002 Page(s):342 - 359
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        Singh, R.; Tretiakov, Y.V.; Johnson, J.B.; Sweeney, S.L.; Barry, R.L.; Kumar, M.; Erturk, M.; Katzer
        Harame, D.L.;
        Electron Devices, IEEE Transactions on
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6. Accurate modeling and parameter extraction for meander-line N-well resistors
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9. An efficient modeling approach for substrate noise coupling analysis
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    Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
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    Digital Object Identifier 10.1109/ISCAS.2002.1010684

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10. A comprehensive geometry-dependent macromodel for substrate noise coupling in heavily ·
    processes
    Ozis, D.; Fiez, T.; Mayaram, K.;
    Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002
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11. Design procedure for fully integrated 900 MHz medium power amplifiers in 0.6 μm CMOS tec
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    Baureis, P.; Peter, M.; Hein, H.; Oehler, F.;
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    Digital Object Identifier 10.1109/SMIC.2001.942356

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☐	L9	(substrate with coupling) and (substrate with noise) and matrix	251
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PGPUB-DOCUMENT-NUMBER: 20040117162
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:
NAME                         CITY              STATE       COUNTRY
Ozis, Dicle                  Seattle           WA          US
Mayaram, Kartikeya           Corvallis         OR          US
Fiez, Terri                  Corvallis         OR          US

US-CL-CURRENT: 703/2
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SUBSTRATES	335473
NOISE	462380
NOISES	43598
NOIZE	78
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PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Term	Documents
SCALABLE	29226
SCALABLES	0
PARAMETER?	0
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☐ 1. Document ID: US 20050254106 A9

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L6: Entry 1 of 4                    File: PGPB              Nov 17, 2005

PGPUB-DOCUMENT-NUMBER: 20050254106
PGPUB-FILING-TYPE: corrected
DOCUMENT-IDENTIFIER: US 20050254106 A9

TITLE: Scanning device for coded data

PUBLICATION-DATE: November 17, 2005

PRIOR-PUBLICATION:
DOC-ID                    DATE
US 0190092 A1             September 30, 2004

INVENTOR-INFORMATION:
NAME                                        CITY        STATE   COUNTRY
Silverbrook, Kia                            Balmain             AU
Lapstun, Paul                               Balmain             AU
Rusman, Jan                                 Balmain             AU
Henderson, Peter Charles Boyd               Balmain             AU
Moini, Alireza                              Balmain             AU
Yourlo, Zhenya Alexander                    Balmain             AU
Underwood, Matthew John                     Balmain             AU
Ridley, Nicholas Damon                      Balmain             AU

US-CL-CURRENT: 358/539; 358/474
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| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 2. Document ID: US 20040190092 A1

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L6: Entry 2 of 4                    File: PGPB              Sep 30, 2004

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TITLE: Scanning device for coded data
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PUBLICATION-DATE: September 30, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Silverbrook, Kia	Balmain		AU
Lapstun, Paul	Balmain		AU
Rusman, Jan	Balmain		AU
Henderson, Peter Charles Boyd	Balmain		AU
Moini, Alireza	Balmain		AU
Yourlo, Zhenya Alexander	Balmain		AU
Underwood, Matthew John	Balmain		AU
Ridley, Nicholas Damon	Balmain		AU

US-CL-CURRENT: <u>358/539</u>; <u>358/474</u>

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PGPUB-FILING-TYPE: new
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TITLE: Sensing device for coded data

PUBLICATION-DATE: September 30, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Silverbrook, Kia	Balmain		AU
Lapstun, Paul	Balmain		AU
Henderson, Peter Charles Boyd	Blamain		AU
Moini, Alireza	Balmain		AU
Yourlo, Zhenya Alexander	Balmain		AU
Underwood, Matthew John	Balmain		AU
Ridley, Nicholas Damon	Balmain		AU
Rusman, Jan	Balmain		AU

US-CL-CURRENT: <u>358/474</u>; <u>358/539</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D |

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L6: Entry 4 of 4   File: PGPB   Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De

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Term	Documents
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PGPUB-DOCUMENT-NUMBER: 20040117162
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Term	Documents
Z	509103
ZES	53
PARAMETER?	0
PARAMETERA	6
PARAMETERB	5
PARAMETERC	1
PARAMETERD	4
PARAMETERE	7
PARAMETERF	1
PARAMETERH	2

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Search Results - Record(s) 1 through 12 of 12 returned.

☐ 1.   Document ID: US 20040117162 A1

Using default format because multiple data bases are involved.

L8: Entry 1 of 12   File: PGPB   Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 2.   Document ID: US 20020022951 A1

L8: Entry 2 of 12   File: PGPB   Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020022951
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020022951 A1

TITLE: Method, apparatus and computer program product for determination of noise in mixed signal systems

PUBLICATION-DATE: February 21, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Heijningen, Marc Van	Leuven		BE
Badaroglu, Mustafa	Leuven		BE

US-CL-CURRENT: 703/16

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 3.   Document ID: US 20010029601 A1

L8: Entry 3 of 12   File: PGPB   Oct 11, 2001

PGPUB-DOCUMENT-NUMBER: 20010029601
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20010029601 A1

TITLE: Semiconductor device analyzer, method for analyzing/manufacturing
semiconductor device, and storage medium storing program for analyzing
semiconductor device

PUBLICATION-DATE: October 11, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Kimura, Tomohisa	Tokyo		JP
Okumura, Makiko	Kanagawa		JP

US-CL-CURRENT: 716/19; 716/4

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 4.   Document ID: US 7016820 B2

L8: Entry 4 of 12   File: USPT   Mar 21, 2006

US-PAT-NO: 7016820
DOCUMENT-IDENTIFIER: US 7016820 B2

TITLE: Semiconductor device analyzer, method for analyzing/manufacturing
semiconductor device, and storage medium storing program for analyzing
semiconductor device

DATE-ISSUED: March 21, 2006

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20010029601 A1	October 11, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kimura; Tomohisa	Tokyo			JP
Okumura; Makiko	Kanagawa			JP

US-CL-CURRENT: 703/2; 700/121, 703/14, 716/4

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 5. Document ID: US 6941258 B2

L8: Entry 5 of 12   File: USPT   Sep 6, 2005

US-PAT-NO: 6941258
DOCUMENT-IDENTIFIER: US 6941258 B2

TITLE: Method, apparatus and computer program product for determination of noise in mixed signal systems

DATE-ISSUED: September 6, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Van Heijningen; Marc	Leuven			BE
Badaroglu; Mustafa	Leuven			BE

US-CL-CURRENT: 703/16; 703/14, 703/15, 703/17, 703/2, 716/6

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 6. Document ID: US 6920417 B2

L8: Entry 6 of 12   File: USPT   Jul 19, 2005

US-PAT-NO: 6920417
DOCUMENT-IDENTIFIER: US 6920417 B2

TITLE: Apparatus for modeling IC substrate noise utilizing improved doping profile access key

DATE-ISSUED: July 19, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lescot; Jerome D.	Grenoble			FR
Marchand; Bertrand L.	Grenoble			FR

US-CL-CURRENT: 703/13; 438/510, 703/2

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 7. Document ID: US 6895344 B2

L8: Entry 7 of 12   File: USPT   May 17, 2005

US-PAT-NO: 6895344

DOCUMENT-IDENTIFIER: US 6895344 B2

TITLE: Extracting resistances for a substrate network

DATE-ISSUED: May 17, 2005

INVENTOR-INFORMATION:
NAME	CITY	STATE	ZIP CODE	COUNTRY
Ramaswamy; Sridhar	Plano	TX		

US-CL-CURRENT: 702/57; 455/3.02, 716/19, 716/6

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 8.   Document ID: US 6725185 B2

L8: Entry 8 of 12   File: USPT   Apr 20, 2004

US-PAT-NO: 6725185
DOCUMENT-IDENTIFIER: US 6725185 B2
** See image for **Certificate of Correction** **

TITLE: Apparatus for modeling IC substrate noise

DATE-ISSUED: April 20, 2004

INVENTOR-INFORMATION:
NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Francois J. R.	Neuchatel			CH

US-CL-CURRENT: 703/14; 438/210, 438/510, 703/2, 716/4

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 9.   Document ID: US 6480986 B1

L8: Entry 9 of 12   File: USPT   Nov 12, 2002

US-PAT-NO: 6480986
DOCUMENT-IDENTIFIER: US 6480986 B1

TITLE: IC substrate noise modeling including extracted capacitance for improved
accuracy

DATE-ISSUED: November 12, 2002

INVENTOR-INFORMATION:
NAME	CITY	STATE	ZIP CODE	COUNTRY
Richer; Jean-Michel	Voiron			FR

US-CL-CURRENT: <u>716/4</u>; <u>438/510</u>, <u>716/1</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐   10.   Document ID: US 6438733 B1

L8: Entry 10 of 12   File: USPT   Aug 20, 2002

US-PAT-NO: 6438733
DOCUMENT-IDENTIFIER: US 6438733 B1

TITLE: IC <u>substrate noise modeling</u> with improved surface gridding technique

DATE-ISSUED: August 20, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Fran.cedilla.ois J. R.	Neuchatel			CH

US-CL-CURRENT: <u>716/4</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐   11.   Document ID: US 6291324 B1

L8: Entry 11 of 12   File: USPT   Sep 18, 2001

US-PAT-NO: 6291324
DOCUMENT-IDENTIFIER: US 6291324 B1

TITLE: Method of <u>modeling</u> IC <u>substrate noises</u> utilizing improved doping profile access

DATE-ISSUED: September 18, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lescot; Jerome D.	Grenoble			FR
Marchand; Bertrand L.	Grenoble			FR

US-CL-CURRENT: <u>438/510</u>; <u>438/424</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐   12.   Document ID: US 6291322 B1

L8: Entry 12 of 12   File: USPT   Sep 18, 2001

US-PAT-NO: 6291322
DOCUMENT-IDENTIFIER: US 6291322 B1

** See image for <u>Certificate of Correction</u> **

TITLE: Method for modeling noises in an integrated circuit

DATE-ISSUED: September 18, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Fran.cedilla.ois J. R.	Neuchatel			CH

US-CL-CURRENT: <u>438/510</u>; <u>438/210</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Term	Documents
SUBSTRATE	815034
SUBSTRATES	335473
MODEL$	0
MODEL	582924
MODELA	20
MODELAA	3
MODELABBRV	1
MODELABILITIES	1
MODELABILITY	21
MODELABLE	65
MODELABSTRACTION	1
(L4 AND (MODEL$ WITH SUBSTRATE)).PGPB,USPT.	12

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☐ 1. Document ID: US 20050023656 A1

Using default format because multiple data bases are involved.

L11: Entry 1 of 2   File: PGPB   Feb 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050023656
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050023656 A1

TITLE: Vertical system integration

PUBLICATION-DATE: February 3, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Leedy, Glenn J.	Saline	MI	US

US-CL-CURRENT: 257/678; 257/686, 438/106, 438/109

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw-De |

☐ 2. Document ID: US 20040117162 A1

L11: Entry 2 of 2   File: PGPB   Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

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NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |